

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,071,739 B1
APPLICATION NO. : 10/755011
DATED : July 4, 2006
INVENTOR(S) : Ivan Duzevik et al.

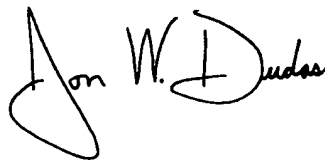
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3, Line 57, Delete "(lout)" and insert -- (Iout) --.
Column 4, Line 59, "lout" and insert -- Iout --.
Column 5, Line 28, Delete "Bias 1,Bias2" and insert -- Bias 1, Bias 2 --.
Column 5, Line 29, Delete "lout" and insert -- Iout --.
Column 8, Line 2, In Claim 14, after "circuit" insert -- , --.
Column 8, Line 43 (Approx.), In Claim 14, after "transistor" insert -- , --.
Column 10, Line 11, In Claim 19, delete "circuit;" and insert -- circuitry; --.
Column 10, Line 20 (Approx.) In Claim 19, delete "tacks" and insert -- tracks --.
Column 10, Line 29 (Approx.) In Claim 19, delete "op amp" and insert
-- operational amplifier --.
Column 10, Line 32 (Approx.) In Claim 19, delete In Claim 19, delete "op amp" and
insert -- operational amplifier --.

Signed and Sealed this

Twelfth Day of December, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS

Director of the United States Patent and Trademark Office